## Module



## Single-phase AC circuits

## Lesson 15

## Solution of Current in AC Series and Parallel <br> Circuits

In the last lesson, two points were described:

1. How to solve for the impedance, and current in an ac circuit, consisting of single element, R / L / C?
2. How to solve for the impedance, and current in an ac circuit, consisting of two elements, R and L/C, in series, and then draw complete phasor diagram?
In this lesson, the solution of currents in simple circuits, consisting of resistance R, inductance $L$ and/or capacitance $C$ connected in series, fed from single phase ac supply, is presented. Then, the circuit with all above components in parallel is taken up. The process of drawing complete phasor diagram with current(s) and voltage drops in the different components is described. The computation of total power and also power consumed in the different components, along with power factor, is explained. One example of series circuit are presented in detail, while the example of parallel circuit will be taken up in the next lesson.

Keywords: Series and parallel circuits, impedance, admittance, power, power factor.
After going through this lesson, the students will be able to answer the following questions;

1. How to compute the total reactance and impedance / admittance, of the series and parallel circuits, fed from single phase ac supply?
2. How to compute the different currents and also voltage drops in the components, both in magnitude and phase, of the circuit?
3. How to draw the complete phasor diagram, showing the currents and voltage drops?
4. How to compute the total power and also power consumed in the different components, along with power factor?

## Solution of Current in R-L-C Series Circuit

## Series (R-L-C) circuit



Fig. 15.1 (a) Circuit diagram
The voltage balance equation for the circuit with $\mathrm{R}, \mathrm{L}$ and C in series (Fig. 15.1a), is $v=R i+L \frac{d i}{d t}+\frac{1}{C} \int i d t=\sqrt{2} V \sin \omega t$

The current, $i$ is of the form,
$i=\sqrt{2} I \sin (\omega t \pm \phi)$
As described in the previous lesson (\#14) on series (R-L) circuit, the current in steady state is sinusoidal in nature. The procedure given here, in brief, is followed to determine the form of current. If the expression for $i=\sqrt{2} I \sin (\omega t-\phi)$ is substituted in the voltage equation, the equation shown here is obtained, with the sides (LHS \& RHS) interchanged.
$R \cdot \sqrt{2} I \sin (\omega t-\phi)+\omega L \cdot \sqrt{2} I \cos (\omega t-\phi)-(1 / \omega C) \cdot \sqrt{2} I \cos (\omega t-\phi)$
$=\sqrt{2} V \sin \omega t$
or $\quad R \cdot \sqrt{2} I \sin (\omega t-\phi)+[\omega L-(1 / \omega C)] \cdot \sqrt{2} I \cos (\omega t-\phi)=\sqrt{2} V \sin \omega t$
The steps to be followed to find the magnitude and phase angle of the current $I$, are same as described there (\#14).

So, the phase angle is $\phi=\tan ^{-1}[\omega L-(1 / \omega C)] / R$
and the magnitude of the current is $I=V / Z$
where the impedance of the series circuit is $Z=\sqrt{R^{2}+[\omega L-(1 / \omega C)]^{2}}$
Alternatively, the steps to find the rms value of the current I, using complex form of impedance, are given here.

The impedance of the circuit is

$$
Z \angle \pm \phi=R+j\left(X_{L}-X_{C}\right)=R+j\left(\omega L-\frac{1}{\omega C}\right)
$$

where,

$$
\begin{aligned}
& Z=\sqrt{R^{2}+\left(X_{L}-X_{C}\right)^{2}}=\sqrt{R^{2}+(\omega L-(1 / \omega C))^{2}}, \text { and } \\
& \phi=\tan ^{-1}\left(\frac{X_{L}-X_{C}}{R}\right)=\tan ^{-1}\left(\frac{\omega L-(1 / \omega C)}{R}\right) \\
& \bar{I} \angle \mp \phi=\frac{\bar{V} \angle 0^{\circ}}{Z \angle \pm \phi}=\frac{V+j 0}{R+j\left(X_{L}-X_{C}\right)}=\frac{V+j 0}{R+j(\omega L-(1 / \omega C))} \\
& I=\frac{V}{Z}=\frac{V}{\sqrt{R^{2}+\left(X_{L}-X_{C}\right)^{2}}}=\frac{V}{\sqrt{R^{2}+\left(\omega L-\frac{1}{\omega C}\right)^{2}}}
\end{aligned}
$$

Two cases are: (a) Inductive $\left(\omega L>\frac{1}{\omega C}\right)$, and (b) Capacitive $\left(\omega L<\frac{1}{\omega C}\right)$.

## (a) Inductive

In this case, the circuit is inductive, as total reactance $(\omega L-(1 / \omega C))$ is positive, under the condition $(\omega L>(1 / \omega C))$. The current lags the voltage by $\phi$ (taken as positive), with the voltage phasor taken as reference. The power factor (lagging) is less than 1 (one), as $0^{\circ} \leq \phi \leq 90^{\circ}$. The complete phasor diagram, with the voltage drops across the
components and input voltage ( $O A$ ), and also current ( $O B$ ), is shown in Fig. 15.1b. The voltage phasor is taken as reference, in all cases. It may be observed that
$V_{O C}(=i R)+V_{C D}\left[=i\left(j X_{L}\right)\right]+V_{D A}\left[=-i\left(j X_{C}\right)\right]=V_{O A}(=i Z)$
using the Kirchoff's second law relating to the voltage in a closed loop. The phasor diagram can also be drawn with the current phasor as reference, as will be shown in the example given here. The expression for the average power is $V I \cos \phi=I^{2} R$. The power is only consumed in the resistance, $R$, but not in inductance/capacitance ( $\mathrm{L} / \mathrm{C}$ ), in all three cases.


Fig 15.1 (b) Phasor diagram
In this case, the circuit is inductive, as total reactance $(\omega L-(1 / \omega C))$ is positive, under the condition $(\omega L>(1 / \omega C))$. The current lags the voltage by $\phi$ (positive). The power factor (lagging) is less than 1 (one), as $0^{\circ} \leq \phi \leq 90^{\circ}$. The complete phasor diagram, with the voltage drops across the components and input voltage ( $O A$ ), and also current ( $O B$ ), is shown in Fig. 15.1b. The voltage phasor is taken as reference, in all cases. It may be observed that
$V_{O C}(=i R)+V_{C D}\left[=i\left(j X_{L}\right)\right]+V_{D A}\left[=-i\left(j X_{C}\right)\right]=V_{O A}(=i Z)$
using the Kirchoff's second law relating to the voltage in a closed loop. The phasor diagram can also be drawn with the current phasor as reference, as will be shown in the example given here. The expression for the average power is $V I \cos \phi=I^{2} R$. The power is only consumed in the resistance, R , but not in inductance/capacitance ( $\mathrm{L} / \mathrm{C}$ ), in all three cases.

## (b) Capacitive



Capacitive ( $\mathrm{X}_{\mathrm{L}}<\mathrm{X}_{\mathrm{C}}$ )
Fig 15.1 (c) Phasor diagram
The circuit is now capacitive, as total reactance $(\omega L-(1 / \omega C))$ is negative, under the condition $(\omega L<(1 / \omega C))$. The current leads the voltage by $\phi$, which is negative as per convention described in the previous lesson. The voltage phasor is taken as reference here. The complete phasor diagram, with the voltage drops across the components and input voltage, and also current, is shown in Fig. 15.1c. The power factor (leading) is less than 1 (one), as $0^{\circ} \leq \phi \leq 90^{\circ}, \phi$ being negative. The expression for the average power remains same as above.

The third case is resistive, as total reactance ( $\omega L-1 / \omega C$ ) is zero ( 0 ), under the condition ( $\omega L=1 / \omega C$ ). The impedance is $Z \angle 0^{\circ}=R+j 0$. The current is now at unity power factor $\left(\phi=0^{\circ}\right)$, i.e. the current and the voltage are in phase. The complete phasor diagram, with the voltage drops across the components and input (supply) voltage, and also current, is shown in Fig. 15.1d. This condition can be termed as 'resonance' in the series circuit, which is described in detail in lesson \#17. The magnitude of the impedance in the circuit is minimum under this condition, with the magnitude of the current being maximum. One more point to be noted here is that the voltage drops in the inductance, L and also in the capacitance, C , is much larger in magnitude than the supply voltage, which is same as the voltage drop in the resistance, R. The phasor diagram has been drawn approximately to scale.


## Resistive ( $\mathrm{X}_{\mathrm{L}}=\mathrm{X}_{\mathrm{C}}$ )

Fig. 15.1 (d) Phasor diagram
It may be observed here that two cases of series (R-L \& R-C) circuits, as discussed in the previous lesson, are obtained in the following way. The first one (inductive) is that of (a), with $C$ very large, i.e.1/ $\omega C \approx 0$, which means that $C$ is not there. The second one (capacitive) is that of (b), with $L$ not being there ( $L$ or $\omega L=0$ ).

## Example 15.1

A resistance, R is connected in series with an iron-cored choke coil ( r in series with L). The circuit (Fig. 15.2a) draws a current of 5 A at $240 \mathrm{~V}, 50 \mathrm{~Hz}$. The voltages across the resistance and the coil are 120 V and 200 V respectively. Calculate,
(a) the resistance, reactance and impedance of the coil,
(b) the power absorbed by the coil, and
(c) the power factor (pf) of the input current.


Fig. 15.2 (a) Circuit diagram

## Solution

$$
I(O B)=5 \mathrm{~A} \quad V_{S}(O A)=240 \mathrm{~V} \quad f=50 \mathrm{~Hz} \quad \omega=2 \pi f
$$

The voltage drop across the resistance $V_{1}(O C)=I \cdot R=120 \mathrm{~V}$

The resistance, $R=V_{1} / I=120 / 5=24 \Omega$
The voltage drop across the coil $V_{2}(C A)=I \cdot Z_{L}=200 \mathrm{~V}$
The impedance of the coil, $Z_{L}=\sqrt{r^{2}+X_{L}^{2}}=V_{2} / I=200 / 5=40 \Omega$ From the phasor diagram (Fig. 15.2b),


Fig. 15.2(b): Phasor Diagram
$\cos \phi=\cos \angle A O C=\frac{O A^{2}+O C^{2}-C A^{2}}{2 \cdot O A \cdot O C}=\frac{(120)^{2}+(240)^{2}-(200)^{2}}{2 \times 120 \times 240}=\frac{32,000}{57,600}$
$=0.556$
The power factor (pf) of the input current $=\cos \phi=0.556$ (lag)
The phase angle of the total impedance, $\phi=\cos ^{-1}(0.556)=56.25^{\circ}$
Input voltage, $V_{S}(O A)=I \cdot Z=240 \mathrm{~V}$
The total impedance of the circuit, $Z=\sqrt{(R+r)^{2}+X_{L}^{2}}=V_{S} / I=240 / 5=48 \Omega$
$Z \angle \phi=(R+r)+j X_{L}=48 \angle 56.25^{\circ}=(26.67+j 39.91) \Omega$
The total resistance of the circuit, $R+r=24+r=26.67 \Omega$
The resistance of the coil, $r=26.67-24.0=2.67 \Omega$
The reactance of the coil, $X_{L}=\omega L=2 \pi f L=39.9 \Omega$
The inductance of the coil, $L=\frac{X_{L}}{2 \pi f}=\frac{39.9}{2 \pi \times 50}=0.127 \mathrm{H}=127 \cdot 10^{-3}=127 \mathrm{mH}$
The phase angle of the coil,
$\phi_{L}=\cos ^{-1}\left(r / Z_{L}\right)=\cos ^{-1}(2.67 / 40.0)=\cos ^{-1}(0.067)=86.17^{\circ}$
$\left.Z_{L} \angle \phi_{L}=r+j X_{L}=(2.67+j 39.9)=40 \angle 86.17^{\circ} 7\right) \Omega$
The power factor (pf) of the coil, $\cos \phi=0.067$ (lag)
The copper loss in the coil $=I^{2} r=5^{2} \times 2.67=66.75 \mathrm{~W}$

## Example 15.2

An inductive coil, having resistance of $8 \Omega$ and inductance of 80 mH , is connected in series with a capacitance of $100 \mu \mathrm{~F}$ across $150 \mathrm{~V}, 50 \mathrm{~Hz}$ supply (Fig. 15.3a). Calculate, (a) the current, (b) the power factor, and (c) the voltages drops in the coil and capacitance respectively.


Fig. 15.3 (a) Circuit diagram

## Solution

$$
\begin{aligned}
& f=50 \mathrm{~Hz} \quad \omega=2 \pi f=2 \pi \times 50=314.16 \mathrm{rad} / \mathrm{s} \\
& \mathrm{~L}=80 \mathrm{mH}=80 \cdot 10^{-3}=0.08 \mathrm{H} \quad X_{l}=\omega L=314.16 \times 0.08=25.13 \Omega \\
& \mathrm{C}=100 \mu \mathrm{~F}=100 \cdot 10^{-6} \mathrm{~F} \quad X_{C}=\frac{1}{\omega C}=\frac{1}{314.16 \times 100 \cdot 10^{-6}}=31.83 \Omega \\
& \mathrm{R}=8 \Omega \quad V_{S}(\mathrm{OA})=150 \mathrm{~V}
\end{aligned}
$$

The impedance of the coil, $Z_{L} \angle \phi_{L}=R+j X_{L}=(8.0+j 25.13)=26.375 \angle 72.34^{\circ} \Omega$ The total impedance of the circuit,

$$
\begin{aligned}
& Z \angle-\phi=R+j\left(X_{L}-X_{C 4}\right)=8.0+j(25.13-31.83)=(8.0-j 6.7) \\
& =10.435 \angle-39.95^{\circ} \Omega
\end{aligned}
$$

The current drawn from the supply,

$$
I \angle \phi=\frac{V \angle 0^{\circ}}{Z \angle-\phi}=\left(\frac{150}{10.435}\right) \angle 39.95^{\circ}=14.375 \angle 39.95^{\circ} A=(11.02+j 9.26) A
$$

The current is, $I=14.375 \mathrm{~A}$
The power factor $(\mathrm{pf})=\cos \phi=\cos 39.95^{\circ}=0.767$ (lead)


Fig. 15.3 (b) Phasor diagram

Please note that the current phasor is taken as reference in the phasor diagram (Fig. 15.3b) and also here. The voltage drop in the coil is,
$V_{1} \angle \theta_{1}=I \angle 0^{\circ} \cdot Z_{L} \angle \phi_{L}=(14.375 \times 26.375) \angle 72.34^{\circ}=379.14 \angle 72.34^{\circ} V$
$=(115.1+j 361.24) V$
The voltage drop in the capacitance in,
$V_{2} \angle \theta_{2}=I \angle 0^{\circ} \cdot Z_{C} \angle-\phi_{C}=(14.475 \times 31.83) \angle-90.0^{\circ}=457.58 \angle-90.0^{\circ} V$
$=-j 457.58 \mathrm{~V}$

## Solution of Current in Parallel Circuit

## Parallel circuit

The circuit with all three elements, R, L \& C connected in parallel (Fig. 15.4a), is fed to the ac supply. The current from the supply can be computed by various methods, of which two are described here.


Fig. 15.4 (a) Circuit diagram.

## First method

The current in three branches are first computed and the total current drawn from the supply is the phasor sum of all three branch currents, by using Kirchoff's first law related to the currents at the node. The voltage phasor $(V)$ is taken as reference.

All currents, i.e. three branch currents and total current, in steady state, are sinusoidal in nature, as the input (supply voltage is sinusoidal of the form,
$v=\sqrt{2} V \sin \omega t$
Three branch currents are obtained by the procedure given in brief.
$v=R \cdot i_{R}$, or $i_{R}=v / R=\sqrt{2}(V / R) \sin \omega t=\sqrt{2} I_{R} \sin \omega t$,
where, $\left|I_{R}\right|=|(V / R)|$
Similarly, $v=L \frac{d i_{L}}{d t}$
So, $i_{L}$ is,
$i_{L}=(1 / L) \int v d t=(1 / L) \int \sqrt{2} V(\sin \omega t) d t=-\sqrt{2}[V /(\omega L)] \cos \omega t=-\sqrt{2} I_{L} \cos \omega t$
$=\sqrt{2} I_{L} \sin \left(\omega t-90^{\circ}\right)$
where, $\left|I_{L}\right|=\left|\left(V / X_{L}\right)\right|$ with $X_{L}=\omega L$
$v=(1 / C) \int i_{C} d t$, from which $i_{C}$ is obtained as,
$i_{C}=C \frac{d v}{d t}=C \frac{d}{d t}(\sqrt{2} V \sin \omega t)=\sqrt{2}(V \cdot \omega C) \cos \omega t=\sqrt{2} I_{C} \cos \omega t$
$=\sqrt{2} I_{C} \sin \left(\omega t+90^{\circ}\right)$
where, $\left|I_{c}\right|=\left|\left(V / X_{C}\right)\right|$ with $X_{C}=(1 / \omega C)$
Total (supply) current, $i$ is
$i=i_{R}+i_{L}+i_{C}=\sqrt{2} I_{R} \sin \omega t-\sqrt{2} I_{L} \cos \omega t+\sqrt{2} I_{C} \cos \omega t$
$=\sqrt{2} I_{R} \sin \omega t-\sqrt{2}\left(I_{L}-I_{C}\right) \cos \omega t=\sqrt{2} I \sin (\omega t \mp \phi)$
The two equations given here are obtained by expanding the trigonometric form appearing in the last term on RHS, into components of $\cos \omega t$ and $\sin \omega t$, and then equating the components of $\cos \omega t$ and $\sin \omega t$ from the last term and last but one (previous) .
$I \cos \phi=I_{R}$ and $I \sin \phi=\left(I_{L}-I_{C}\right)$
From these equations, the magnitude and phase angle of the total (supply) current are,

$$
\begin{aligned}
& |I|=\sqrt{\left(I_{R}\right)^{2}+\left(I_{L}-I_{C}\right)^{2}}=|V| \cdot \sqrt{\left(\frac{1}{R}\right)^{2}+\left(\frac{1}{X_{L}}-\frac{1}{X_{C}}\right)^{2}} \\
& =|V| \cdot \sqrt{\left(\frac{1}{R}\right)^{2}+\left(\frac{1}{\omega L}-\omega C\right)^{2}}=|V| \cdot|Y| \\
& \phi=\tan ^{-1}\left(\frac{I_{L}-I_{C}}{I_{R}}\right)=\tan ^{-1}\left(\frac{\left(1 / X_{L}\right)-\left(1 / X_{C}\right)}{(1 / R)}\right)=\tan ^{-1}\left[R \cdot\left(\frac{1}{X_{I}}-\frac{1}{X_{C}}\right)\right] \\
& =\tan ^{-1}\left[R \cdot\left(\frac{1}{\omega L}-\omega C\right)\right]
\end{aligned}
$$

where, the magnitude of the term (admittance of the circuit) is,

$$
|Y|=\sqrt{\left(\frac{1}{R}\right)^{2}+\left(\frac{1}{X_{L}}-\frac{1}{X_{C}}\right)^{2}}=\sqrt{\left(\frac{1}{R}\right)^{2}+\left(\frac{1}{\omega L}-\omega C\right)^{2}}
$$

Please note that the admittance, which is reciprocal of impedance, is a complex quantity. The angle of admittance or impedance, is same as the phase angle, $\phi$ of the current $I$, with the input (supply) voltage taken as reference phasor, as given earlier.

Alternatively, the steps required to find the rms values of three branch currents and the total (suuply) current, using complex form of impedance, are given here.

Three branch currents are

$$
\begin{aligned}
& I_{R} \angle 0^{\circ}=I_{R}=\frac{V}{R} ; \quad I_{L} \angle-90^{\circ}=-j I_{L}=\frac{V}{j X_{L}}=\frac{V}{j \omega L}=-j \frac{V}{\omega L} \\
& I_{C} \angle+90^{\circ}=j I_{C}=\frac{V}{-j X_{C}}=\frac{V}{-j(1 / \omega C)}=j \omega C V
\end{aligned}
$$

Of the three branches, the first one consists of resistance only, the current, $I_{R}$ is in phase with the voltage (V). In the second branch, the current, $I_{L}$ lags the voltage by $90^{\circ}$, as there is inductance only, while in the third one having capacitance only, the current, $I_{C}$ leads the voltage $90^{\circ}$. All these cases have been presented in the previous lesson.

The total current is
$I \angle \pm \phi=I_{R}+j\left(I_{c}-I_{L}\right)=V\left[\frac{1}{R}+j\left(\omega C-\frac{1}{\omega L}\right)\right]$
where,

$$
\begin{aligned}
& I=\sqrt{I_{R}^{2}+\left(I_{C}-I_{L}\right)^{2}}=V \sqrt{\left[\frac{1}{R^{2}}+\left(\omega C-\frac{1}{\omega L}\right)^{2}\right]}, \text { and } \\
& \phi=\tan ^{-1}\left(\frac{I_{C}-I_{L}}{I_{R}}\right)=\tan ^{-1}\left[R\left(\omega C-\frac{1}{\omega L}\right)\right]
\end{aligned}
$$

The two cases are as described earlier in series circuit.

## (a) Inductive



Fig. 15.4 (b) Phasor diagram
In this case, the circuit being inductive, the current lags the voltage by $\phi$ (positive), as $I_{L}>I_{C}$, i.e. $1 / \omega L>\omega C$, or $\omega L<1 / \omega C$. This condition is in contrast to that derived in the case of series circuit earlier. The power factor is less than 1 (one). The complete phasor diagram, with the three branch currents along with total current, and also the voltage, is shown in Fig. 15.4b. The voltage phasor is taken as reference in all cases. It may be observed there that
$I_{R}(O D)+I_{L}(D C)+I_{C}(C B)=I(O B)$

The Kirchoff's first law related to the currents at the node is applied, as stated above. The expression for the average power is $V I \cos \phi=I_{R}^{2} R=V^{2} / R$. The power is only consumed in the resistance, $R$, but not in inductance/capacitance ( $\mathrm{L} / \mathrm{C}$ ), in all three cases.

## (b) Capacitive

The circuit is capacitive, as $I_{C}>I_{L}$, i.e. $\omega C>1 / \omega L$, or $\omega L>1 / \omega C$. The current leads the voltage by $\phi$ ( $\phi$ being negative), with the power factor less than 1 (one). The complete phasor diagram, with the three branch currents along with total current, and also the voltage, is shown in Fig. 15.4c.

The third case is resistive, as $\left|I_{L}\right|=\left|I_{C}\right|$, i.e. $1 / \omega L=\omega C$ or $\omega L=1 / \omega C$. This is the same condition, as obtained in the case of series circuit. It may be noted that two currents, $I_{L}$ and $I_{C}$, are equal in magnitude as shown, but opposite in sign (phase difference being $180^{\circ}$ ), and the sum of these currents $\left(I_{L}+I_{C}\right)$ is zero ( 0 ). The total current is in phase with the voltage $\left(\phi=0^{\circ}\right)$, with $|I|=\left|I_{R}\right|$, the power factor being unity. The complete phasor diagram, with the three branch currents along with total current, and also the voltage, is shown in Fig. 15.4d. This condition can be termed as 'resonance' in the parallel circuit, which is described in detail in lesson \#17. The magnitude of the impedance in the circuit is maximum (i.e., the magnitude of the admittance is minimum) under this condition, with the magnitude of the total (supply) current being minimum.


Capacitive ( $\mathbf{I}_{\mathrm{L}}<\mathrm{I}_{\mathrm{C}}$ )
Fig. 15.4 (c) Phasor diagram

The circuit with two elements, say R \& L, can be solved, or derived with C being large ( $I_{C}=0$ or $1 / \omega C=0$ ).


$$
\text { Resistive }\left(\mathbf{I}_{\mathbf{L}}=\mathbf{I}_{\mathrm{C}}\right)
$$

Fig. 15.4 (d) Phasor Diagram

## Second method

Before going into the details of this method, the term, Admittance must be explained. In the case of two resistance connected in series, the equivalent resistance is the sum of two resistances, the resistance being scalar (positive). If two impedances are connected in series, the equivalent impedance is the sum of two impedances, all impedances being complex. Please note that the two terms, real and imaginary, of two impedances and also the equivalent one, may be positive or negative. This was explained in lesson no. 12.

If two resistances are connected in parallel, the inverse of the equivalent resistance is the sum of the inverse of the two resistances. If two impedances are connected in parallel, the inverse of the equivalent impedance is the sum of the inverse of the two impedances. The inverse or reciprocal of the impedance is termed 'Admittance', which is complex. Mathematically, this is expressed as

$$
Y=\frac{1}{Z}=\frac{1}{Z_{1}}+\frac{1}{Z_{2}}=Y_{1}+Y_{2}
$$

As admittance ( Y ) is complex, its real and imaginary parts are called conductance (G) and susceptance (B) respectively. So, $Y=G+j B$. If impedance, $Z \angle \phi=R+j X$ with X being positive, then the admittance is

$$
\begin{aligned}
& Y \angle-\phi=\frac{1}{Z \angle 0^{\circ}}=\frac{1}{R+j X}=\frac{R-j X}{(R+j X)(R-j X)}=\frac{R-j X}{R^{2}+X^{2}} \\
& =\frac{R}{R^{2}+X^{2}}-j \frac{X}{R^{2}+X^{2}}=G-j B
\end{aligned}
$$

where,

$$
G=\frac{R}{R^{2}+X^{2}} ; \quad B=\frac{X}{R^{2}+X^{2}}
$$

Please note the way in which the result of the division of two complex quantities is obtained. Both the numerator and the denominator are multiplied by the complex conjugate of the denominator, so as to make the denominator a real quantity. This has also been explained in lesson no. 12.

The magnitude and phase angle of Z and Y are

$$
\begin{aligned}
& Z=\sqrt{R^{2}+X^{2}} ; \quad \phi=\tan ^{-1}(X / R), \text { and } \\
& Y=\sqrt{G^{2}+B^{2}}=\frac{1}{\sqrt{R^{2}+X^{2}}} ; \quad \phi=\tan ^{-1}\left(\frac{B}{G}\right)=\tan ^{-1}\left(\frac{X}{R}\right)
\end{aligned}
$$

To obtain the current in the circuit (Fig. 15.4a), the steps are given here.
The admittances of the three branches are

$$
\begin{aligned}
& Y_{1} \angle 0^{\circ}=\frac{1}{Z_{1}}=\frac{1}{R} ; \quad Y_{2} \angle-90^{\circ}=\frac{1}{Z_{2}}=\frac{1}{j X_{L}}=-j \frac{1}{\omega L} \\
& Y_{3} \angle 90^{\circ}=\frac{1}{Z_{3}}=\frac{1}{-j X_{C}}=j \omega C
\end{aligned}
$$

The total admittance, obtained by the phasor sum of the three branch admittances, is

$$
Y \angle \pm \phi=Y_{1}+Y_{2}+Y_{3}=\frac{1}{R}+j\left(\omega C-\frac{1}{\omega L}\right)=G+j B
$$

where,

$$
\begin{aligned}
& Y=\sqrt{\left[\frac{1}{R^{2}}+\left(\omega C-\frac{1}{\omega L}\right)^{2}\right]} ; \quad \phi=\tan ^{-1}\left[R\left(\omega C-\frac{1}{\omega L}\right)\right], \text { and } \\
& G=1 / R ; \quad B=\omega C-1 / \omega L
\end{aligned}
$$

The total impedance of the circuit is

$$
Z \angle \mp \phi=\frac{1}{Y \angle \pm \phi}=\frac{1}{G+j B}=\frac{G}{G^{2}+B^{2}}-j \frac{B}{G^{2}+B^{2}}
$$

The total current in the circuit is obtained as

$$
I \angle \pm \phi=\frac{V \angle 0^{\circ}}{Z \angle \mp \phi}=V \angle 0^{\circ} \cdot Y \angle \pm \phi=(V Y) \angle \pm \phi
$$

where the magnitude of current is $I=V \cdot Y=V / Z$
The current is the same as obtained earlier, with the value of $Y$ substituted in the above equation.

This is best illustrated with an example, which is described in the next lesson.
The solution of the current in the series-parallel circuits will also be discussed there, along with some examples.

## Problems

15.1 Calculate the current and power factor (lagging / leading) for the following circuits (Fig. 15.5a-d), fed from an ac supply of 200 V. Also draw the phasor diagram in all cases.


Fig. 15.5
15.2 A voltage of 200 V is applied to a pure resistor ( R ), a pure capacitor, C and a lossy inductor coil, all of them connected in parallel. The total current is 2.4 A , while the component currents are 1.5, 2.0 and 1.2 A respectively. Find the total power factor and also the power factor of the coil. Draw the phasor diagram.
15.3 A 200 V .50 Hz supply is connected to a lamp having a rating of $100 \mathrm{~V}, 200 \mathrm{~W}$, in series with a pure inductance, L , such that the total power consumed is the same, i.e. 200 W . Find the value of L .

A capacitance, C is now connected across the supply. Find value of C, to bring the supply power factor to unity (1.0). Draw the phasor diagram in the second case.
1.(a) Find the value of the load resistance $\left(\mathrm{R}_{\mathrm{L}}\right)$ to be connected in series with a real voltage source ( $\mathrm{V}_{\mathrm{S}}+\mathrm{R}_{\mathrm{S}}$ in series), such that maximum power is transferred from the above source to the load resistance.
(b) Find the voltage was $8 \Omega$ resistance in the circuit shown in Fig. 1(b).
2.(a) Find the Theremin's equivalent circuit (draw the ckt.) between the terminals $\mathrm{A}+\mathrm{B}$, of the circuit shown in Fig. 2(a).
(b) A circuit shown in Fig. 2(b) is supplied at $40 \mathrm{~V}, 50 \mathrm{~Hz}$. The two voltages $\mathrm{V}_{1}$ and $\mathrm{V}_{2}$ (magnitude only) is measured as 60 V and 25 V respectively. If the current, I is measured as 1 A , find the values of $\mathrm{R}, \mathrm{L}$ and C . Also find the power factor of the circuit (R-L-C). Draw the complete phasor diagram.
3.(a) Find the line current, power factor, and active (real) power drawn from 3-phase, $100 \mathrm{~V}, 50 \mathrm{~Hz}$, balanced supply in the circuit shown in Fig. 3(a).
(b) In the circuit shown in Fig. 3(b), the switch, S is put in position 1 at $\mathrm{t}=0$. Find $\mathrm{i}_{\mathrm{e}}(\mathrm{t})$, $\mathrm{t}>0$, if $v_{\mathrm{c}}\left(0^{-}\right)=6 \mathrm{~V}$. After the circuit reaches steady state, the switch, S is brought to position 2 , at $t=T_{1}$. Find $i_{c}(t), t>T_{1}$. Switch the above waveform.
4.(a) Find the average and rms values of the periodic waveform shown in Fig. 4(a).
(b) A coil of 1 mH lowing a series resistance of $1 \Omega$ is connected in parallel with a capacitor, C and the combination is fed from $100 \mathrm{mV}(0.1 \mathrm{~V}), 1 \mathrm{kHz}$ supply (source) having an internal resistance of $10 \Omega$. If the circuit draws power at unity power factor (upf), determine the value of the capacitor, quality factor of the coil, and power drawn by the circuit. Also draw the phasor diagram.

## List of Figures

Fig. 15.1 (a) Circuit diagram (R-L-C in series)
(b) Phasor diagram - Circuit is inductive ( $X_{I}>X_{C}$ )
(c) Phasor diagram - Capacitive ( $\mathrm{X}_{1}<\mathrm{X}_{\mathrm{C}}$ )
(d) Phasor diagram - Resistive $\left(X_{l}=X_{C}\right)$

Fig. 15.2 (a) Circuit diagram (Ex. 15.1), (b) Phasor diagram
Fig. 15.3 (a) Circuit diagram (Ex. 15.2), (b) Phasor diagram
Fig. 15.4 (a) Circuit diagram (R-L-C in parallel)
(b) Phasor diagram - Circuit is inductive ( $X_{I}<X_{C}$ )
(c) Phasor diagram - Capacitive ( $X_{l}>X_{C}$ )
(d) Phasor diagram - Resistive $\left(X_{l}=X_{C}\right)$

